

09/03/2006 MON 17:34 FAX 3129960855 Freescale Semiconductor --- Bergere, Charles

001/010

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23125 7590 02/27/2006

FREESCALE SEMICONDUCTOR, INC.
 LAW DEPARTMENT
 7700 WEST FARMER LANE MD:TX32/PL02
 AUSTIN, TX 78729

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01 FC:1501 1400.00 DA
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JESLYN GOH (Depositor's name)
 (Signature)
 9 MARCH 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/613,703	07/07/2003	Fusida Harun	SC12793MP	5066

TITLE OF INVENTION: BONDING PAD FOR A PACKAGED INTEGRATED CIRCUIT

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/30/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
WILLIAMS, ALEXANDER O	2826	257-774000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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CHARLES BERGERE

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

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FREESCALE SEMICONDUCTOR, INC

AUSTIN, TEXAS

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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CHARLES BERGERE

Date 9 MARCH 2006

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FROM: Charles Bergere (65) 6513-2609
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Docket No.: SC12793MP
Applicant: Fuaida Harun et al
Serial No.: 10/613,703
Art Unit: 2826
Filed: 07/07/2003

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2.	x	1 page PTOL-85B Issue Fee Transmittal (in duplicate)

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